

Appln No. 10/727,198  
Amdt. Dated December 13, 2005  
Response to Office Action of November 14, 2005

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**COPY****Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently amended) An integrated circuit including an on-board system clock, the integrated circuit including a clock filter configured to ~~determine a~~ detect an under temperature condition of the integrated circuit, at which the temperature is below a predetermined temperature, and to alter an output of the system clock in the event that the an under temperature condition is below a ~~predetermined temperature~~ detected.
2. (Cancelled)
3. (Previously Presented) An integrated circuit according to claim 1, wherein altering the output includes preventing the clock signal from reaching one or more logical circuits on the integrated circuit to which it would otherwise be applied.
4. (Currently Amended) An integrated circuit according to claim 3, wherein the ~~predetermined temperature range is selected~~ such that a temperature-related speed of the system clock output that is not due to the clock filter is within a predetermined frequency range.
5. (Original) An integrated circuit according to claim 4, wherein the frequency range is within an operating frequency of some or all of the logic circuitry to which the system clock is supplied.
6. (Currently Amended) An integrated circuit according to claim 4, wherein the predetermined temperature range is bounded at a lower level is such that an output of the system clock is prevented from reaching some or all of the logic circuitry prior to race conditions due to low temperature causing unpredictable logical behaviour.